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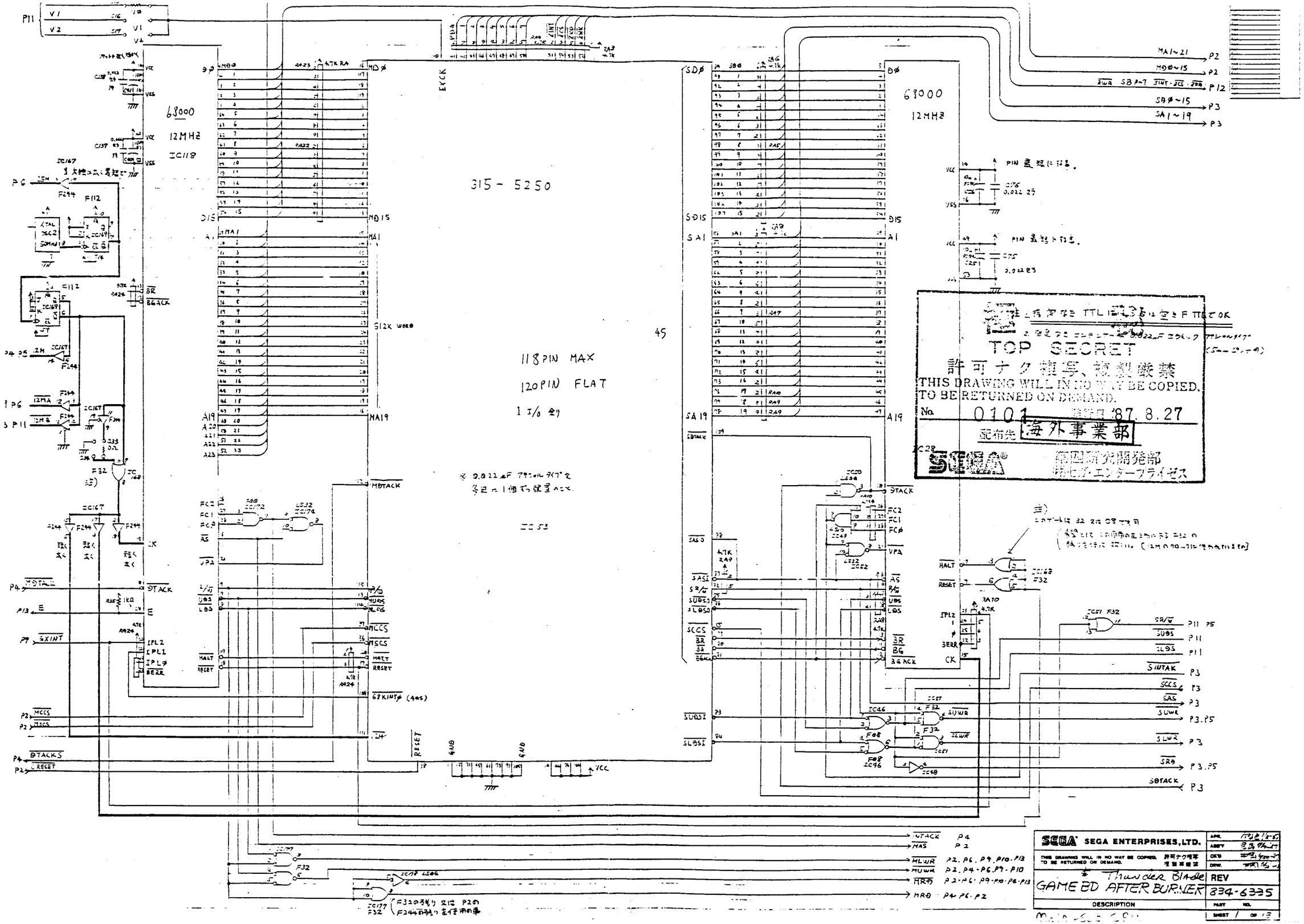
68000
12MHZ
IC118
315-5250
118PIN MAX
120PIN FLAT
1 I/O 余り
68000
12MHZ
TOP SECRET
許可ナク複写、複製厳禁
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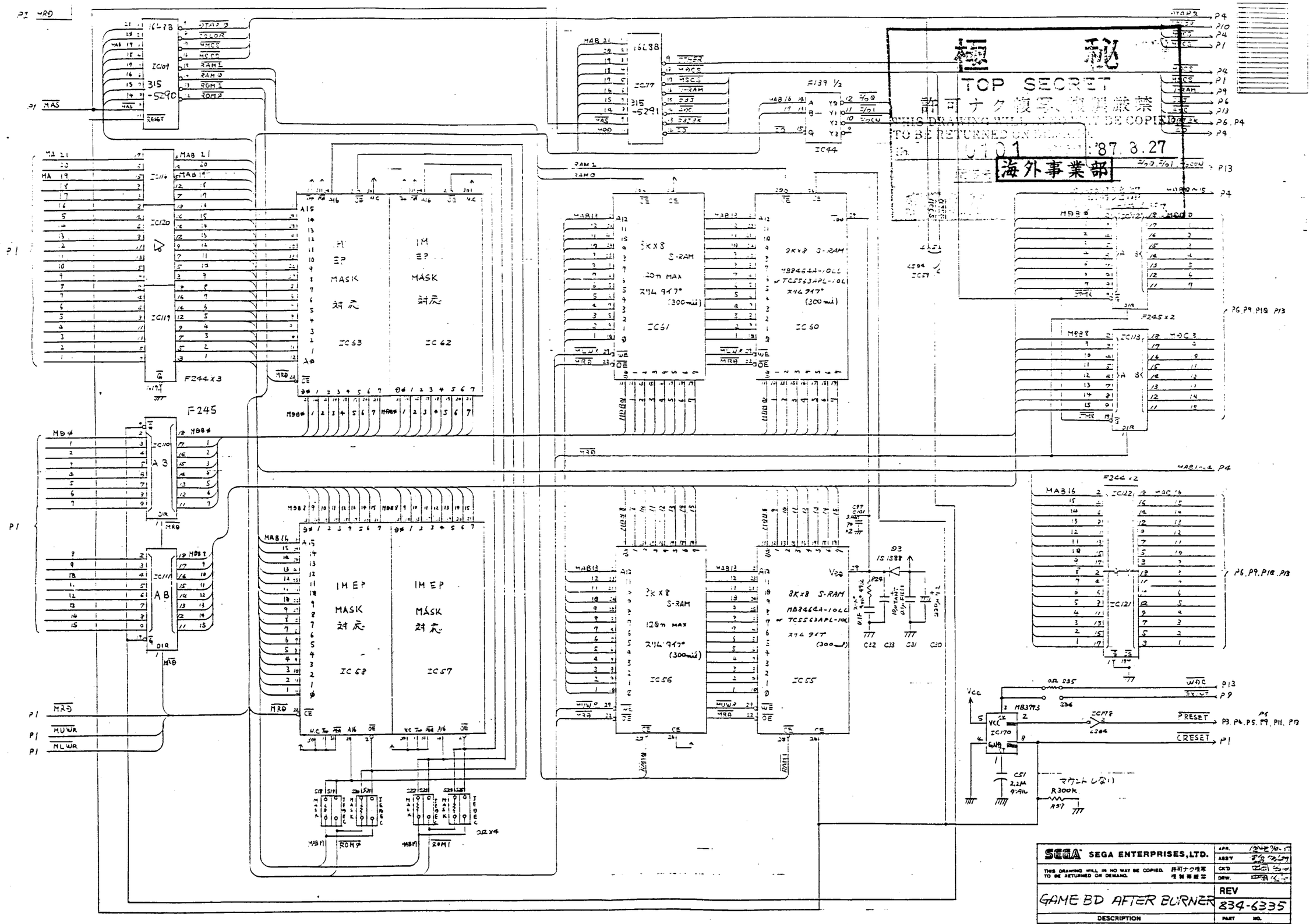
No. 0101
配布先 海外事業部
SEGA
SEGA ENTERPRISES,LTD.
GAME BD AFTER BURNER
834-6335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87. 8. 27
海外事業部
IC107 315-5290
IC77 315-5291
IC44
IC116
IC120
IC119
F244x3
F245
IC110
IC111
IC63 1M EP MASK 対応
IC62 1M EP MASK 対応
IC68 1M EP MASK 対応
IC57 1M EP MASK 対応
IC61 8Kx8 S-RAM 20n MAX HM4-4847 (300mil)
IC60 8Kx8 S-RAM MB8464A-10LL or TC5563APL-10L HM4-4847 (300mil)
IC56 8Kx8 S-RAM 120n MAX HM4-4847 (300mil)
IC55 8Kx8 S-RAM MB8464A-10LL or TC5563APL-10L HM4-4847 (300mil)
IC59 4584
IC113
IC122
IC121
F245x2
F244x2
D3 1S1588
IC170 MB3773
IC178 2204
PRESET
CRESET
C51 2.2μ 9.4V
R300k
SEGA SEGA ENTERPRISES,LTD.
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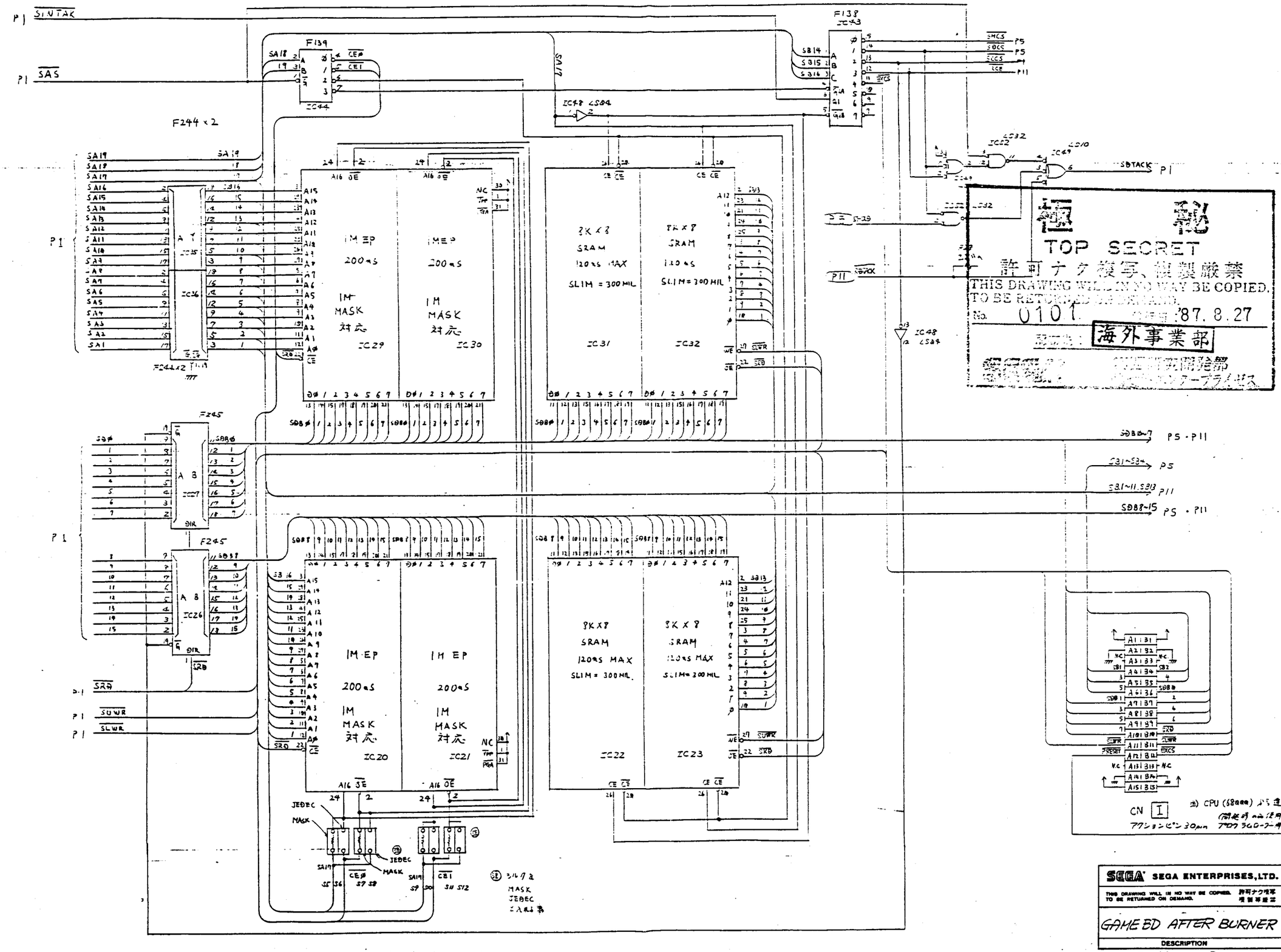
APR.
ASSY
CK'D
DRW.
GAME BD AFTER BURNER
REV
834-6335
DESCRIPTION
PART NO.
SHEET 2 OF 13


SINTAK
SAS
F139
IC44
F244 x 2
IC48 LS04
F138
IC43
SMCS
SOCS
SCCS
SCE
SDTACK
IC22 LS32
IC49 LS10
IC42 LS32
S-29
SDTACK
IC48 LS04
IC35
IC36
1M EP
200nS
1M MASK 対応
IC29
IC30
8K x 8
SRAM
120nS MAX
SL1M = 300 MIL
IC31
IC32
F245
IC27
IC26
SDB7 P5・P11
SB1~SB4 P5
SB1~11, SB13 P11
SDB8~15 P5・P11
IC20
IC21
IC22
IC23
SRD
SUWR
SLWR
JEDEC
MASK
CN I
アクションピン30pin
PRESET P2
TOP SECRET
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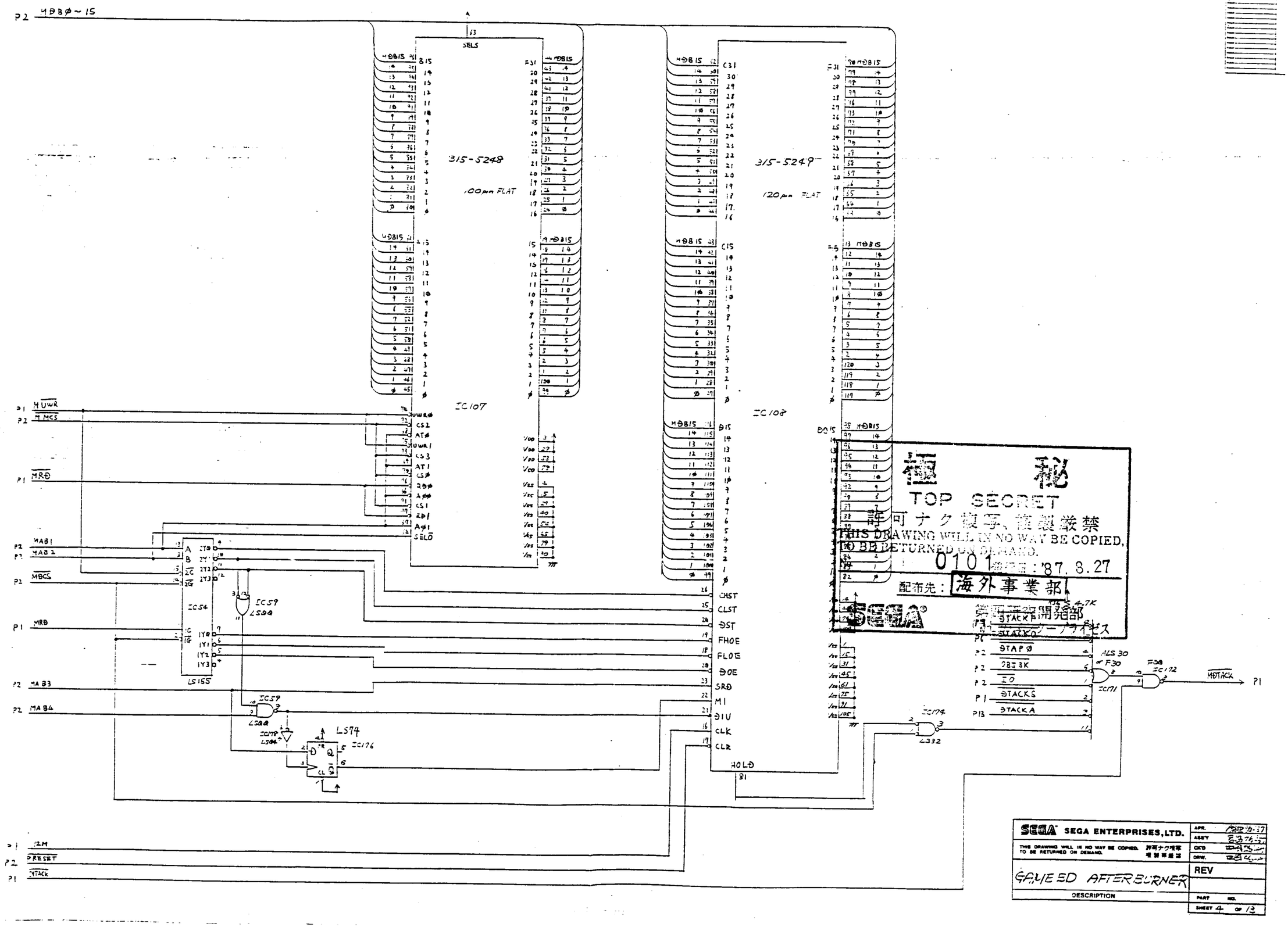
GAME BD AFTER BURNER
834-6335
DESCRIPTION
Sub CPU Rom & Ram
REV
SHEET 3 OF 13


P2 MDB0~15
SELS
315-5248
100pin FLAT
IC107
315-5249
120pin FLAT
IC108
P1 MUWR
P2 MMCS
P1 MRD
P2 MAB1
P2 MAB2
P2 MBCS
P1 MRD
P2 MAB3
P2 MAB4
IC54
LS155
IC59
LS04
LS74
IC176
IC174
LS32
IC171
ALS30
IC172
F09
MDTACK
P1
CHST
CLST
DST
FHOE
FLOE
DOE
SRD
M1
DIU
CLK
CLR
HOLD
STACKB
STACKA
STAP0
DBI3K
IO
STACKS
STACKA
P1 12M
P2 PRESET
P1 DTACK
極秘
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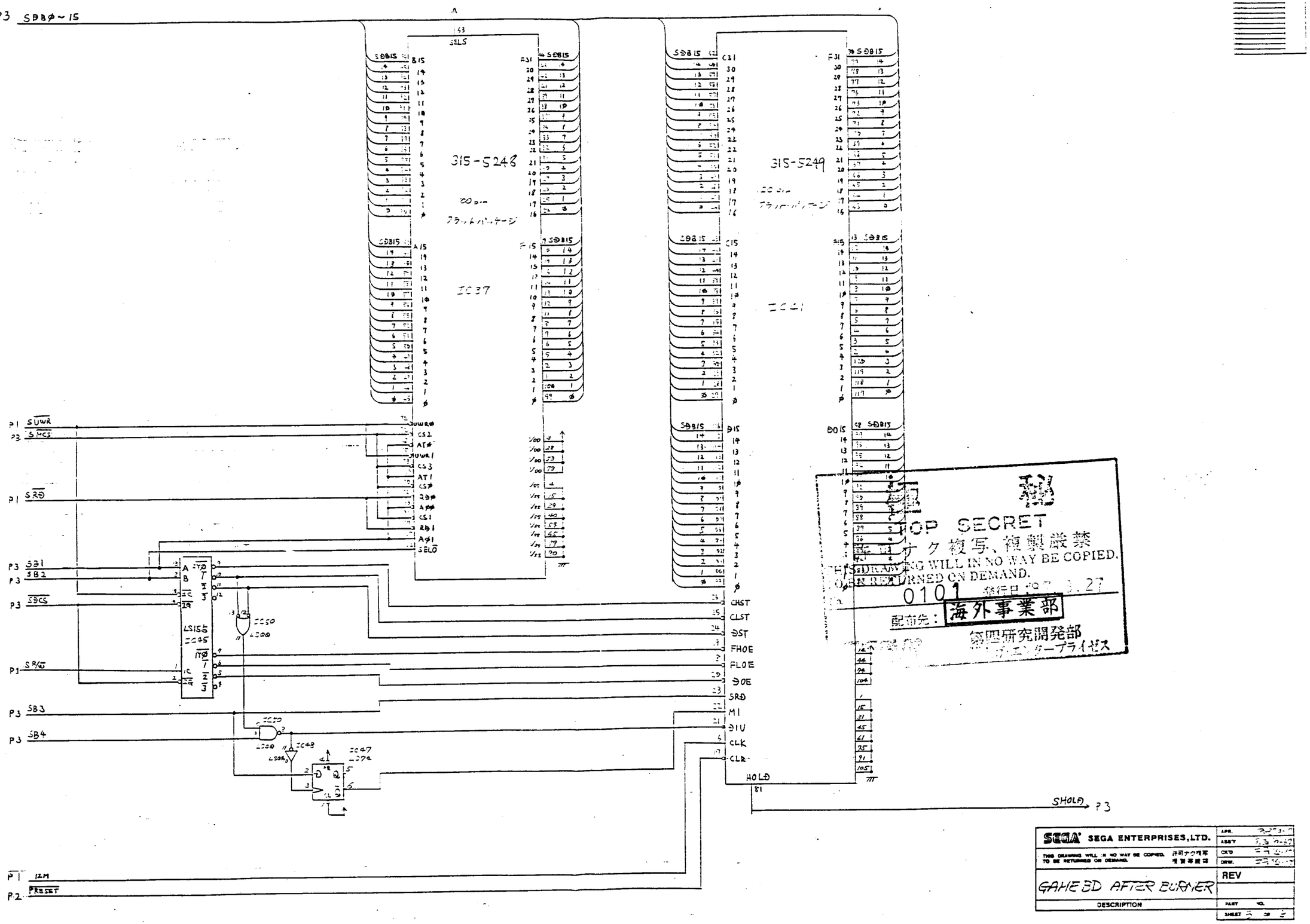
APR.
ASS'Y
CK'D
DRW.
GALIEED AFTER BURNER
DESCRIPTION
REV
PART NO.
SHEET 4 OF 13


P3 SD8Ø~15
SELS
315-5248
IC37
315-5249
IC41
P1 SUWR
P3 SMCS
P1 SRD
P3 SB1
P3 SB2
P3 SBCS
P1 SR/W
P3 SB3
P3 SB4
LS155
IC45
IC50
IC48
IC47
CHST
CLST
DST
FHOE
FLOE
DOE
SRD
M1
DIU
CLK
CLR
HOLD
SHOLD P3
P1 12M
P2 PRESET
TOP SECRET
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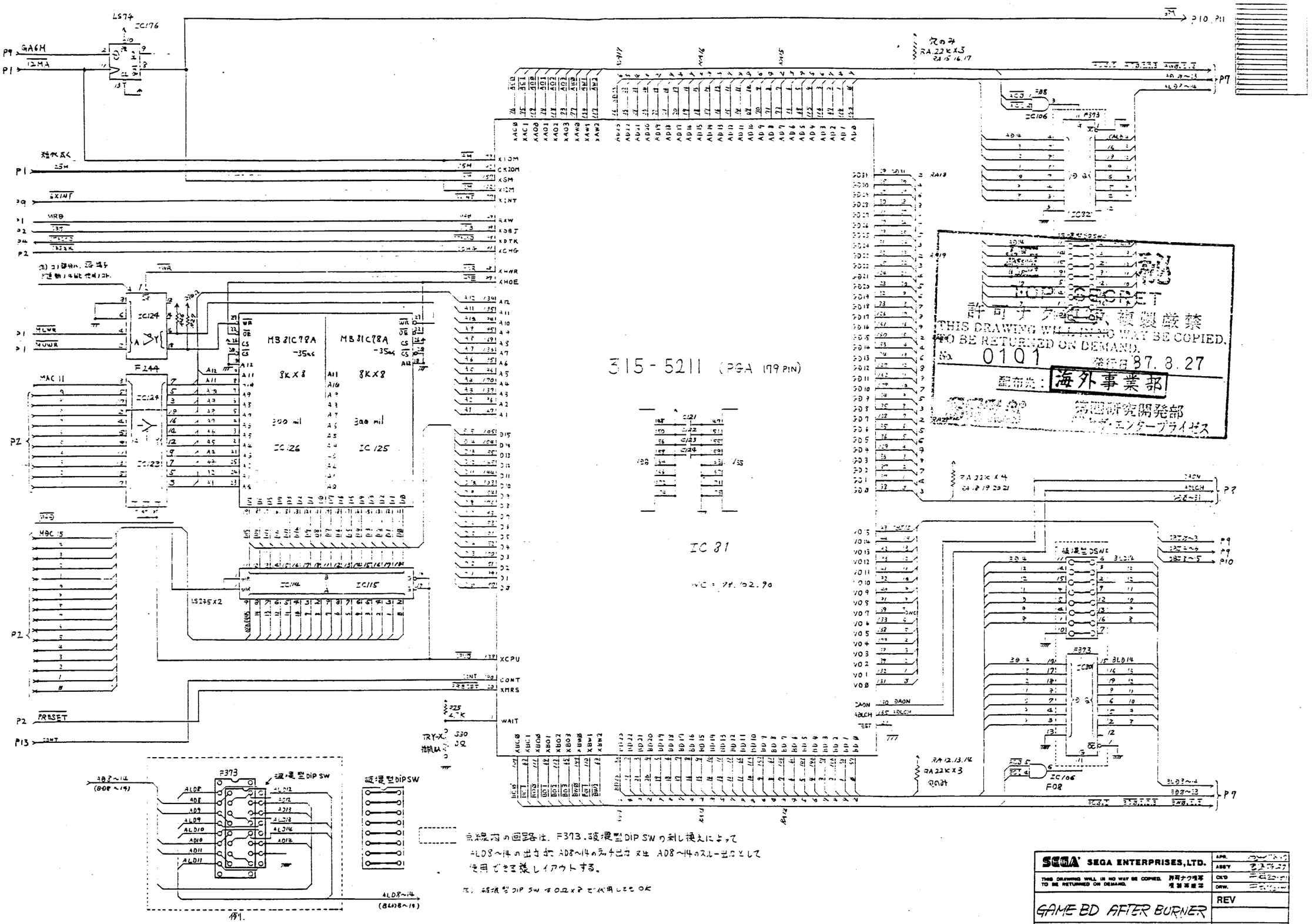
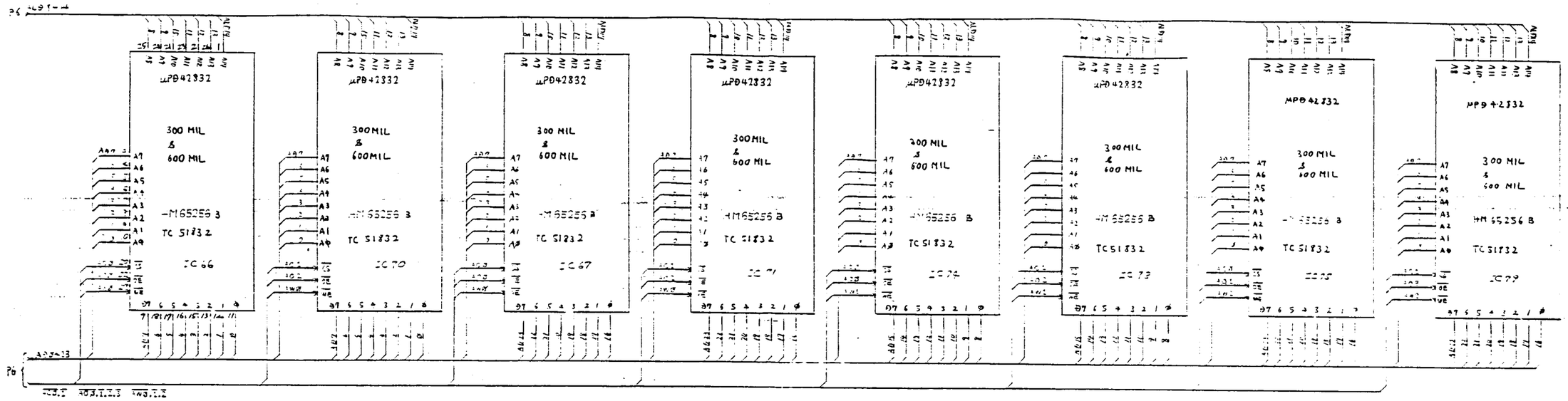
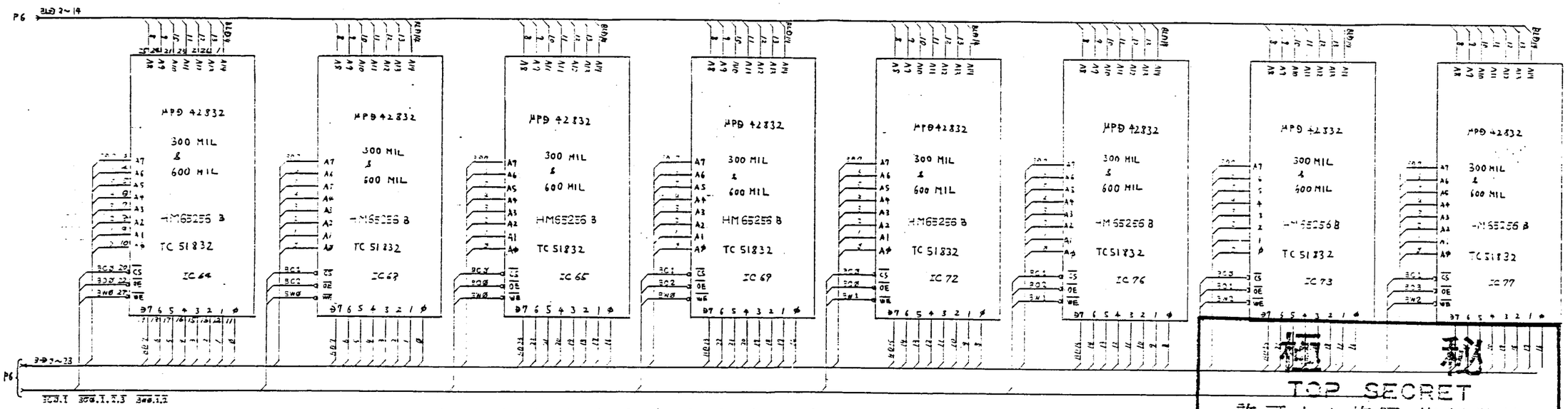
GAME BD AFTER BURNER
DESCRIPTION
REV
PART NO.
SHEET



点線内の回路は、F373、破線型DIP SWの刺し換えによって
ALD8～14の出力がAD8～14のラッチ出力 又は AD8～14のスルー出力として
使用できる様レイアウトする。

注）破線型DIP SWは0Ω×8で代用してもOK





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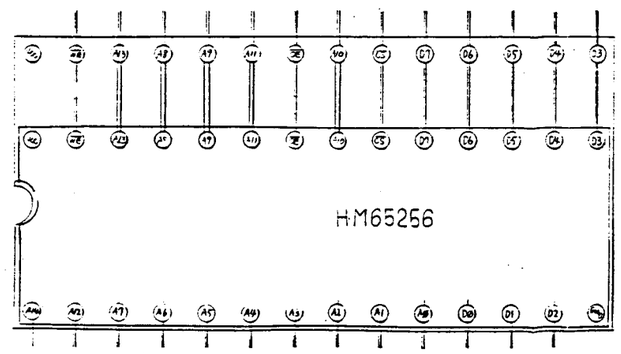
第二研究開発部
セガ・エンタープライゼス

例



HM65256B μPD42832 . TC51832 について.

1. HM65256B を使用する時は PS で DIP SW を使用す
2. μPD42832 or TC51832 を使用する場合は DIP SW
3. アクセスタイムは Acc 120n とする

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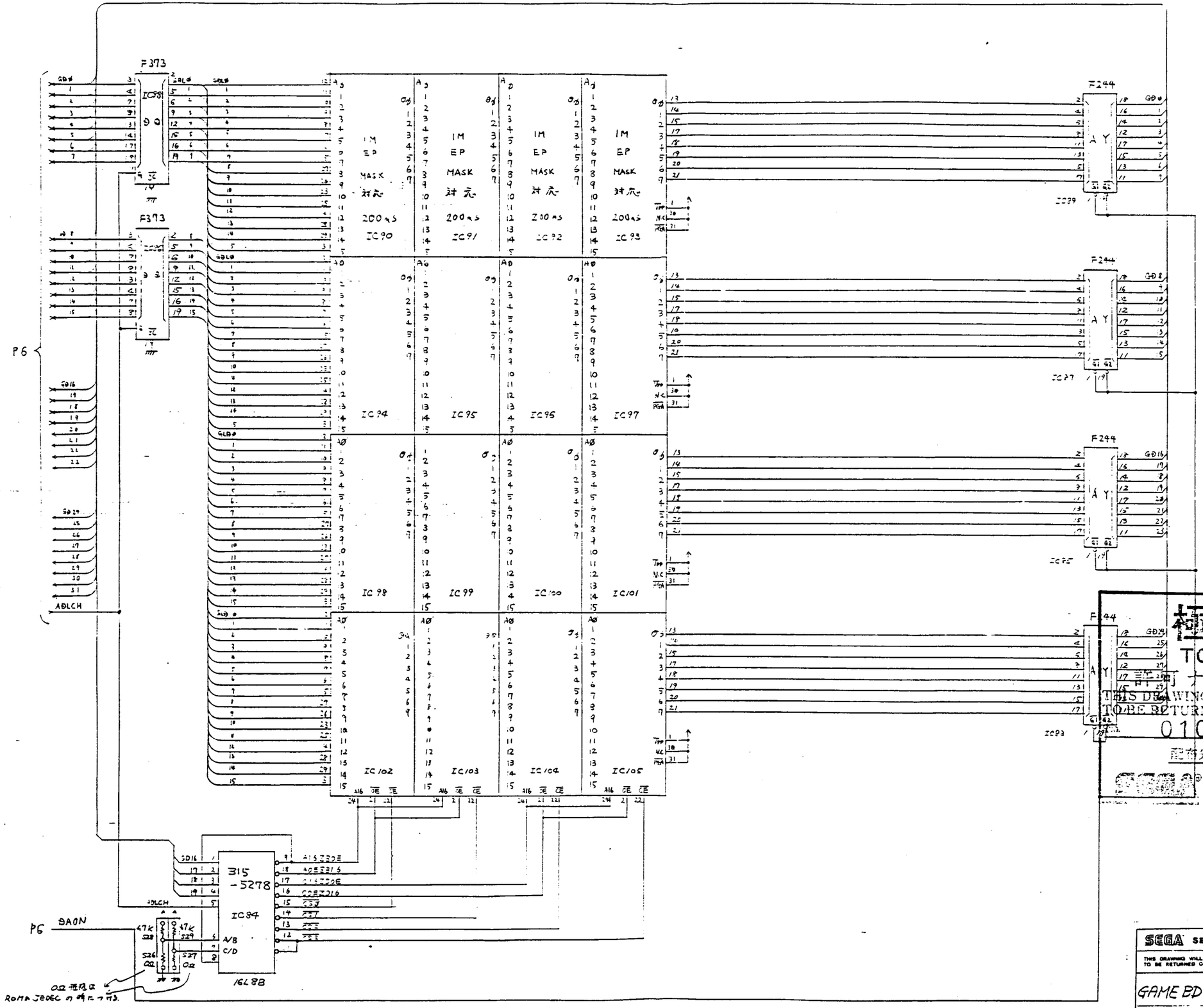
	CK'D
	DRW.
GAME BD AFTER BURNER	REV
DESCRIPTION	PART NO.
	SHEET 17 OF 18

Motion Burns



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ASSY	
CKD	
DRW	

GAME BD AFTER BURNER

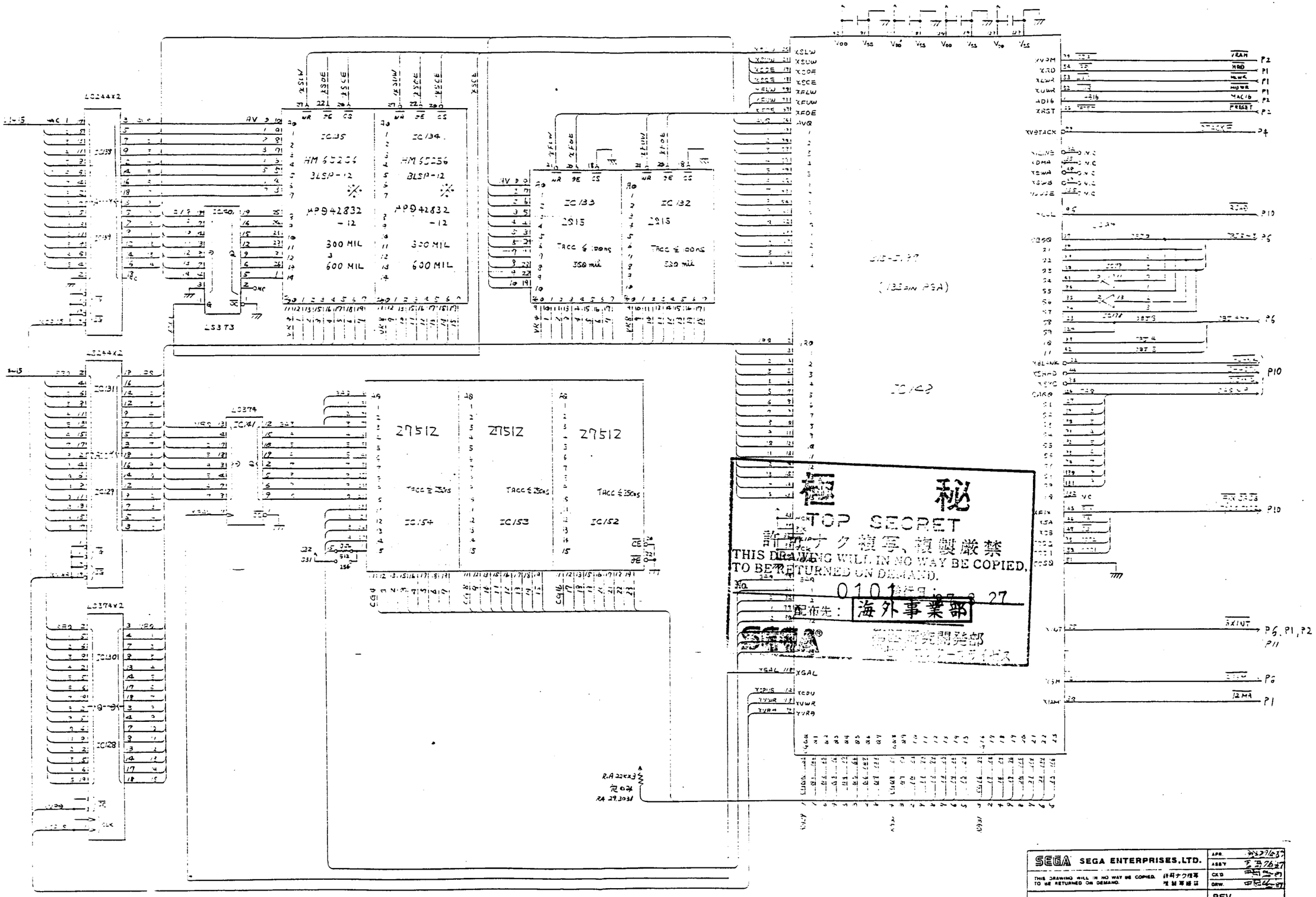
DESCRIPTION

REV

PART NO.

SHEET 3 OF 13

Motion [illegible]


IC/35
HM 65256
BLSP-12
μPD42832
-12
300 MIL
600 MIL
IC/34
IC/133
2815
IC/132
IC/43
(132pin PGA)
27512
IC/54
IC/53
IC/52
LS244x2
LS374
LS374x2
LS373
IC/129
IC/131
IC/130
IC/128
R.A 22kx3
R.A 29.3031
P2
P1
P4
P10
P5
P6
P11
P0
P7
XINT
12MA
TOP SECRET
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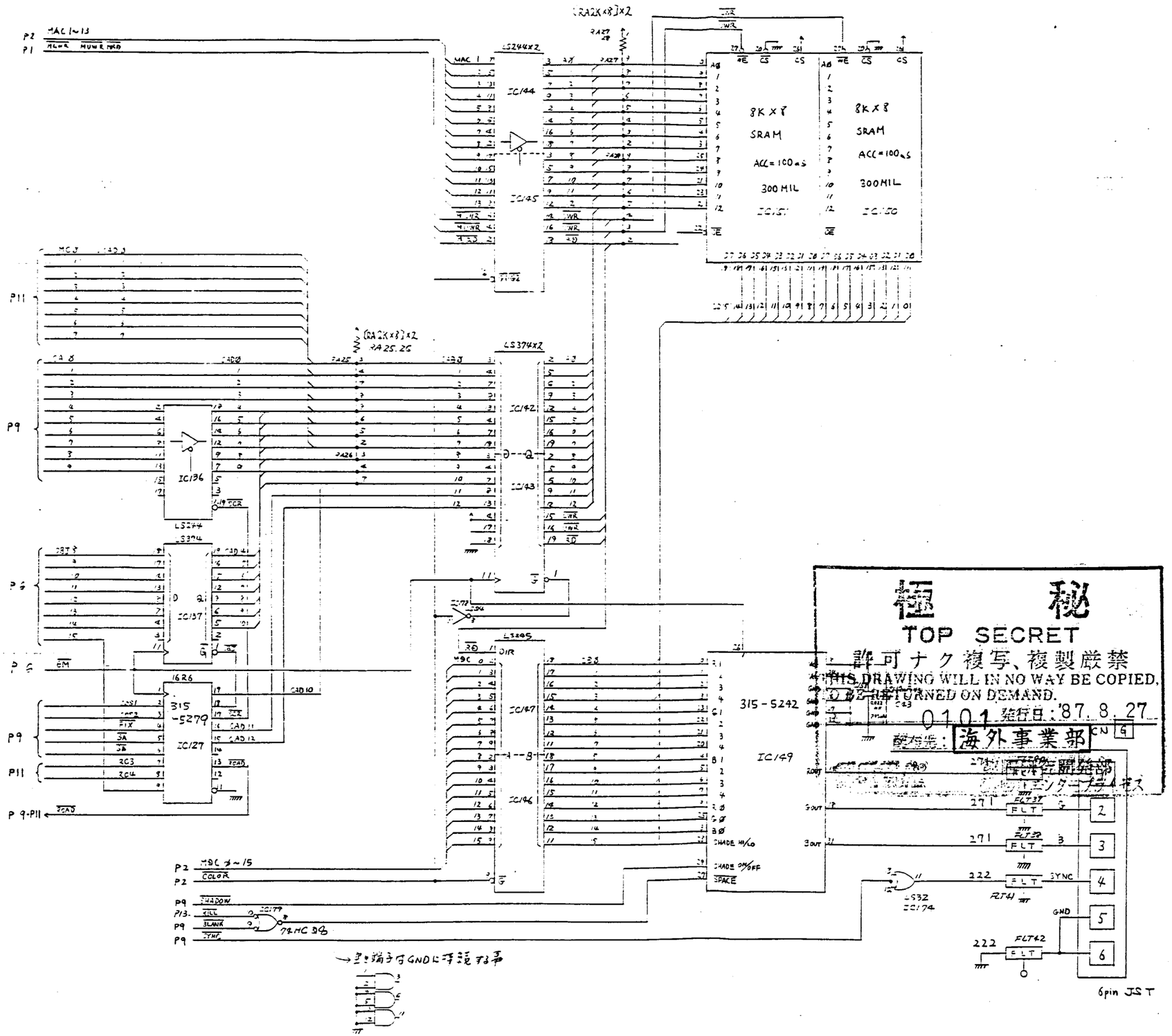
SAME ED AFTER BURNER
DESCRIPTION
REV
PART NO.
SHEET 7 of 13
Sel KG/Grand CKT



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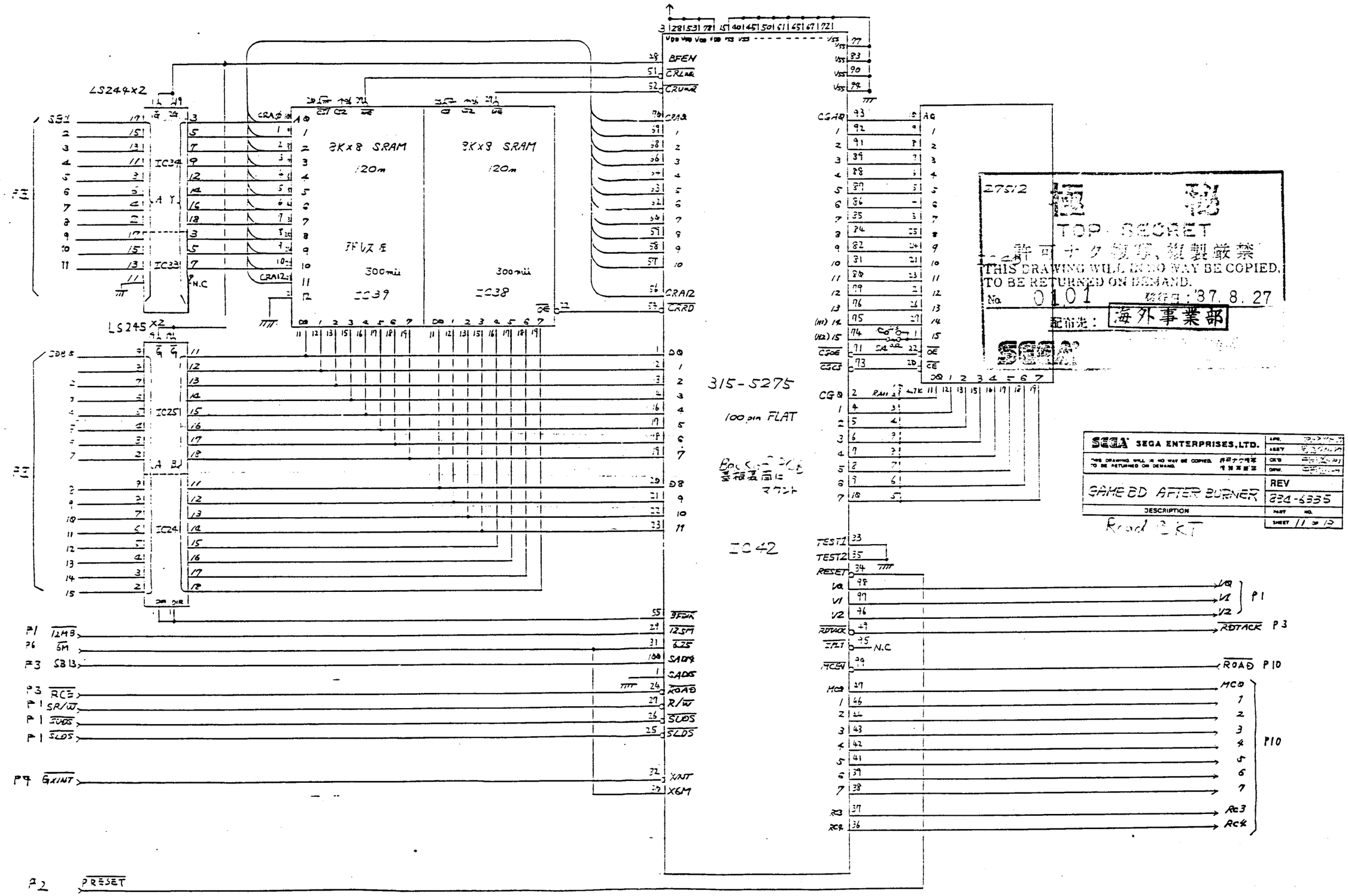
	CKD	
	DRW.	
GAME 3D AFTER BURNER	REV	
DESCRIPTION	PART NO.	
Color Output PCB	SHEET 10 OF 12	


LS244×2
IC34
IC33
N.C
LS245×2
IC25
IC24
3K×8 SRAM
120n
IC39
IC38
300mil
315-5275
100pin FLAT
IC42
27512
TOP SECRET
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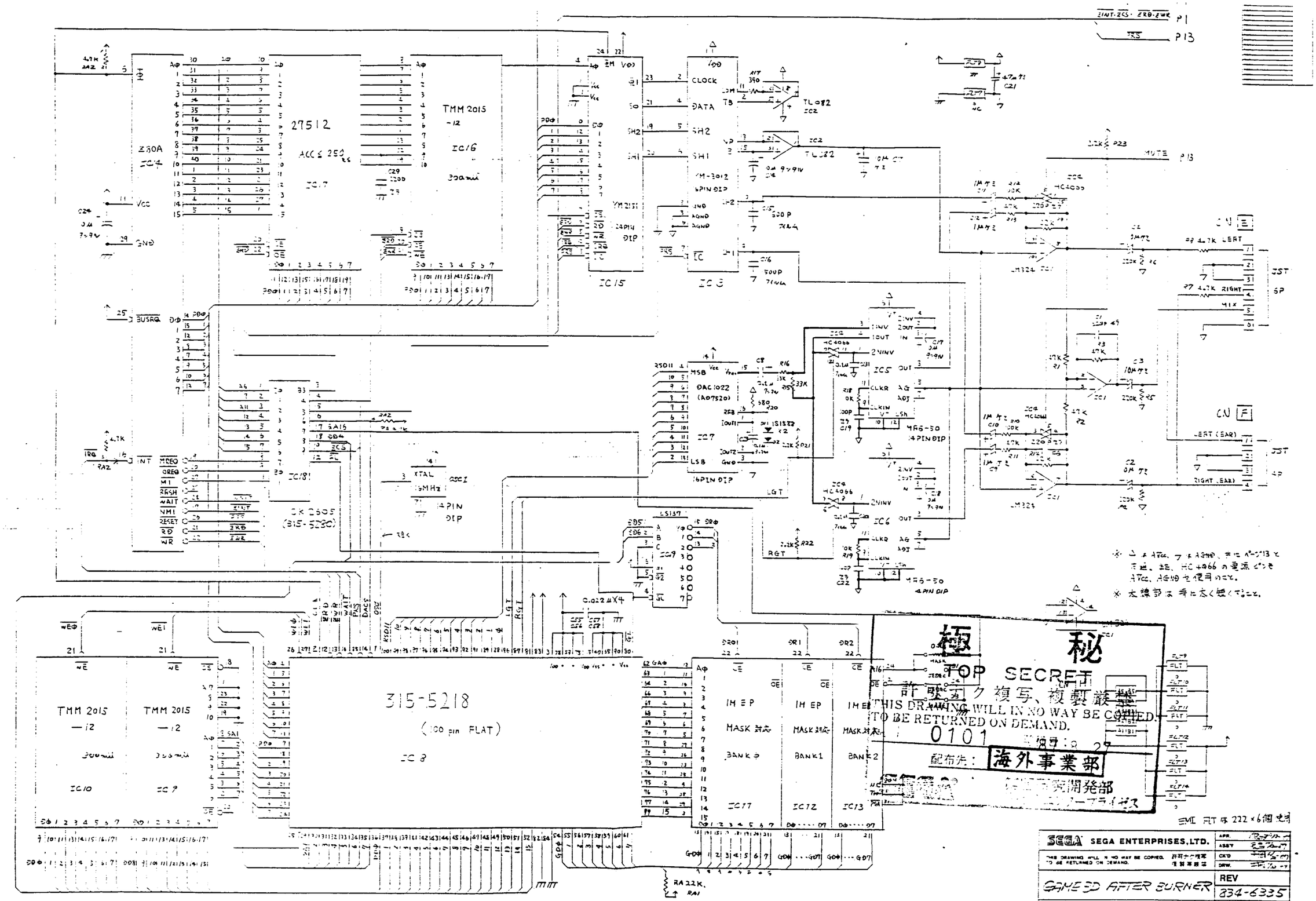
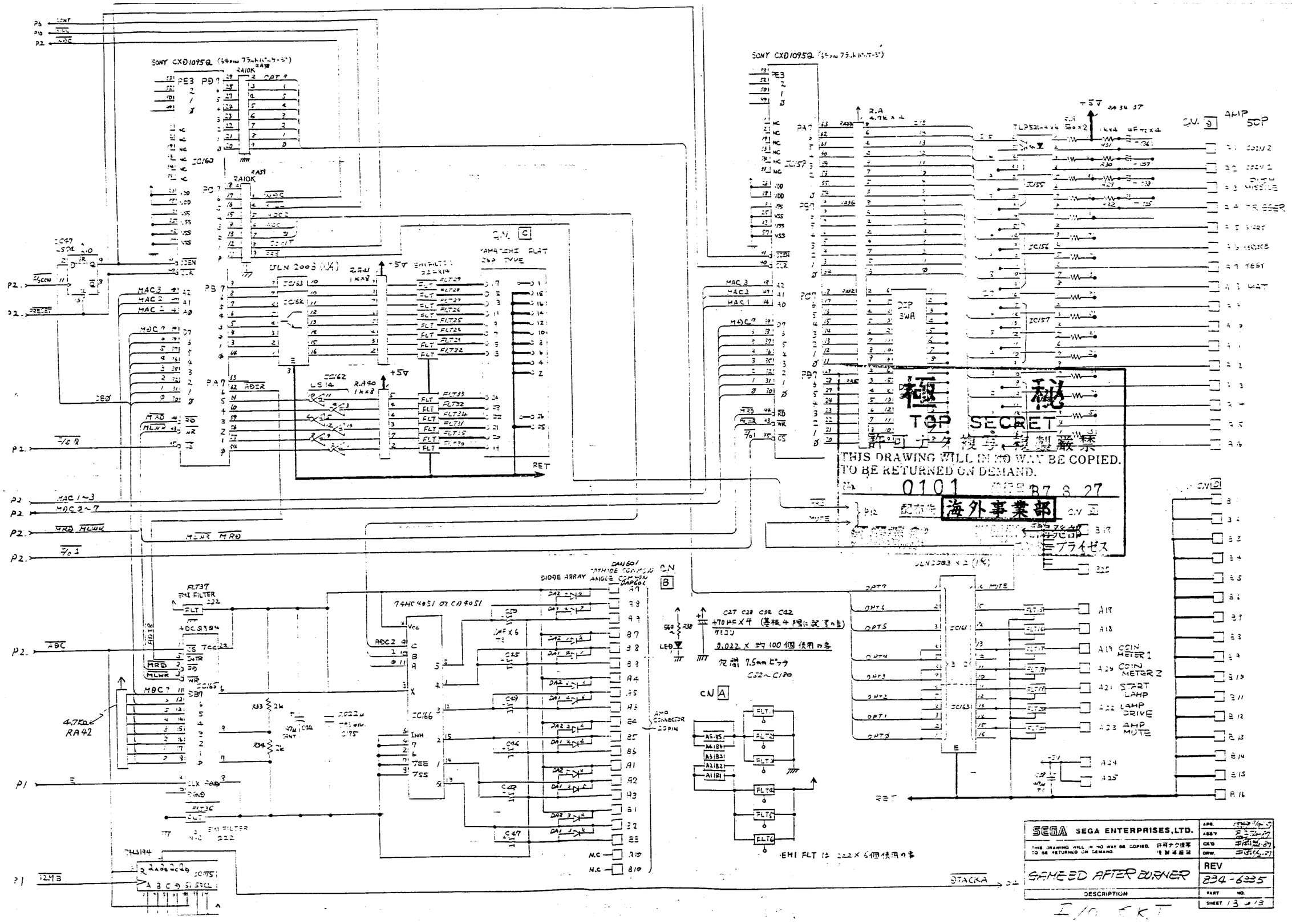
SEGA ENTERPRISES,LTD.
SAME BD AFTER BURNER
834-6335
REV
Road Kit
BFEN
CRLWR
CRUWR
CRA12
CRRD
DB
D8
BFDK
12SM
6.25
SADK
SADS
ROAD
R/W
SLDS
XINT
X6M
PRESET
TEST1
TEST2
RESET
RDTACK
MCS
MC0
RC3
RC4
P1
P3
P10
P2


27512
ACC ≤ 250ns
IC17
TMM 2015
-12
IC16
300mil
Z80A
IC14
IC15
YM2151
24PIN DIP
YM-3012
6PIN DIP
IC3
CLOCK
DATA
SH2
SH1
TL082
IC2
IC4
HC4066
LM324
CN E
CN F
JST
SP
LEFT
RIGHT
MIX
LEFT (EAR)
RIGHT (EAR)
MUTE
P13
P1
DAC1022
(AD7520)
IC7
16PIN DIP
IC5
IC6
MR5-50
14PIN DIP
4PIN DIP
LS157
IC9
CK2505
(315-5230)
XTAL
16MHz
14PIN DIP
IC18
IC8
315-5218
(100 pin FLAT)
TMM 2015
-12
300mil
IC10
IC9
IM EP
MASK 対応
BANK0
BANK1
BANK2
IC77
IC72
IC73
0.022μX4
RA 22K
RA1
BUSRQ
IRQ
INT
MREQ
IOREQ
M1
RFSH
WAIT
NMI
RESET
RD
WR
WE0
WE1
CS
CS1
CS2
CS3
CS4
CS5
CS6
CS7
CS8
CS9
CS10
CS11
CS12
CS13
CS14
CS15
CS16
CS17
CS18
CS19
CS20
CS21
CS22
CS23
CS24
CS25
CS26
CS27
CS28
CS29
CS30
CS31
CS32
CS33
CS34
CS35
CS36
CS37
CS38
CS39
CS40


SONY CXD1095Q
ULN2003 (1/4)
YAMAHA FLAT CRT TYPE
74HC4051 or CD4051
DIODE ARRAY
ULN2003 x 2 (1/4)
AMP 50P
COIN METER 1
COIN METER 2
START LAMP
LAMP DRIVE
AMP MUTE
EMI FILTER
74LS194
RET
STACKA
C27 C28 C39 C42
470μF x 4 (基板4隅に装着の事)
0.022 x 約100個 使用の事
沒間 7.5mm ピッチ
C52～C180
EMI FLT は 222 x 64個使用の事
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834-6335
REV
DESCRIPTION
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SHEET 13 of 13
I/O CKT